

Application No. 10/695,167

MXIC 1518-2
(P900223USDIV1)

In the claims:

This listing of claims will replace all prior versions and listings of claims in the application:

1. (Currently amended) A wave-shaped capacitor, formed over a base conductive layer, said base conductive layer over a base insulator layer on a die, the capacitor including:
a wave-shaped pattern in the base conductive layer comprising at least two adjacent trenches in the base conductive layer;
a multilayer structure contoured over the base conductive layer, the multilayer structure comprising:
a first metal plate layer in electrical contact with the base conductive layer;
an insulating layer over the first plate layer;
a second metal plate layer over the insulating layer; and
[[a]] an interconnect layer over the multilayer structure, including at least one interconnection with the second plate layer.

2. (Original) The device of claim 1, wherein the at least two adjacent trenches are formed by a lithographic or direct writing process and the multilayer structure has a thickness along the sidewalls of the trench that is less than half of a minimum feature size of the lithographic or direct writing process.

3. (Currently amended) The device of claim 1, wherein the base conductive layer and the first ~~conductive~~ metal plate layer are the same structure.

4. (Previously presented) The device of claim 3, wherein the at least two adjacent trenches are formed by a lithographic or direct writing process and the multilayer structure has a thickness along the sidewalls of the trench that is less than half of a minimum feature size of the lithographic or direct writing process.

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